Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (previously presented) An integrated circuit comprising:

a signal transmission channel including radio frequencies; and

an integrated tester to test radio characteristics of said integrated circuit, wherein said tester is independent of said signal transmission channel, said tester comprising:

first means for recovering a part of a signal generated by the transmission channel at a first frequency,

second means for converting said recovered signal from the first frequency into a second frequency,

an amplifier for amplifying said signal at this second frequency, and

a rectifier for rectifying said signal.

2. (previously presented) An integrated circuit as claimed in claim 1, wherein the tester further comprises detection means for detecting the validity of the signal generated by the transmission channel.

3. (previously presented) An integrated circuit as claimed in claim 1, wherein the tester further comprises a filter for filtering harmonics of the signal.

4. (previously presented) An integrated circuit as claimed in claim 1, wherein the first frequency is a radio frequency and the second frequency is a low frequency.

5. (previously presented) A method of testing an integrated circuit comprising a signal transmission channel including radio frequencies, said method to test radio characteristics of said integrated circuit and being independent of said transmission channel, said method comprising:

recovering a part of a signal generated by the transmission channel at a first frequency,

converting the first frequency of the recovered signal into a second frequency,

amplifying said signal at this second frequency, and

rectifying said signal.

6. (previously presented) A method of testing an integrated circuit as claimed in claim 5, further comprising detecting the validity of the signal generated by the transmission channel.

7. (previously presented) A method of testing an integrated circuit as claimed in claim 5, comprising filtering harmonics of said signal.

8. (previously presented) A tester for testing radio characteristics of a transmission channel of an integrated circuit, said tester configured to be integrated with said integrated circuit and to be independent of said signal transmission channel, said tester comprising:

first means for recovering a part of the signal generated by the transmission channel at a first frequency,

second means for converting said signal recovered from the first frequency into a second frequency,

an amplifier for amplifying said signal at this second frequency, and

a rectifier for rectifying said signal.

9. (previously presented) A tester as claimed by claim 8, further comprising detection means for detecting the validity of the signal generated by the transmission channel.

10. (previously presented) A tester as claimed by claim 8, further comprising a filter for filtering harmonics of said signal.

11. (previously presented) A transmitter comprising an integrated circuit comprising a tester as claimed in claim 8.

12. (previously presented) An integrated circuit as claimed in claim 1, wherein said tester is further configured to output a comparison signal separately from said signal transmission channel.

13. (previously presented) An integrated circuit as claimed in claim 12, wherein said tester is further configured to output the comparison signal along a signal path separate from an antenna signal path.

14. (previously presented) An integrated circuit as claimed in claim 1, wherein said first means is further configured to recover about 1/1000 of the signal generated by the transmission channel, wherein the first means possesses an attenuation of about 30 dB.

15. (previously presented) An integrated circuit as claimed in claim 2, wherein the detection means is configured to detect the validity of a power level of the signal generated by the transmission channel to verify that the power level is within an expected range.

16. (previously presented) An integrated circuit as claimed in claim 2, wherein the detection means is configured to detect a spectral purity of the signal generated by the transmission channel.

17. (previously presented) A method of testing an integrated circuit as claimed in claim 5, further comprising outputting a comparison signal separately from said signal transmission channel along a signal path separate from an antenna signal path.

18. (previously presented) A method of testing an integrated circuit as claimed in claim 5, wherein recovering the part of the signal generated by the transmission channel further comprises recovering about 1/1000 of the signal generated by the transmission channel for an attenuation of about 30 dB.

19. (previously presented) A method of testing an integrated circuit as claimed in claim 5, wherein detecting the validity of the signal generated by the transmission channel further comprises detecting one or both of:

a validity of a power level of the signal generated by the transmission channel to verify that the power level is within an expected range; and

a spectral purity of the signal generated by the transmission channel.

20. (previously presented) A tester as claimed by claim 8, wherein said tester is further configured to output a comparison signal separately from said signal transmission channel along a signal path separate from an antenna signal path.